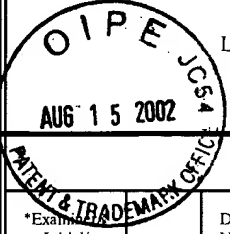
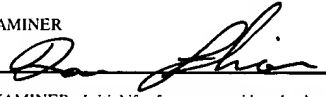


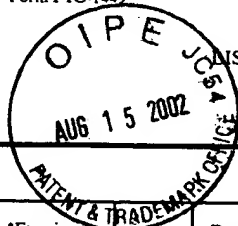


Form PTO-1449		U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE		ATTY. DOCKET NO. IS9-018	SERIAL NO. 09/905, 187		
				LIST OF ART CITED BY APPLICANT (Use several sheets if necessary)			
				APPLICANT Richard C. Eden et al.			
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							Translation Yes No
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DF	AR	"Synchronous Rectification with Adaptive Timing Control"; Acker et al.; 06/95; pps. 88-95.					
DF	AS	"High-Efficiency Low-Voltage DC-DC Conversion for Portable Applications"; Stratakos; The 12th International Symposium on Power Semiconductor Devices & ICs; 1998; pps. 1-245.					
DF	AT	"A Low Voltage CMOS Compatible Power MOSFET for On-Chip Dynamic Voltage Scaling DC-DC Converters" Nassif-Khalil et al.; 1/2000; pps. 43-46					
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				GROUP 2812			
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*Examiner/ Initial/		Document Number	Date	Name	Class	Subclass	Filing Date If Appropriate
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		AH					
		AI					
		AM					
		AN					
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OTHER REFERENCES (including Author, Title, Date, Pertinent Pages, Etc.)							
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DP	AS		"International Rectifier Introduces HexFET® Power MOS in FlipFET™ Packages Enabling World's Highest Power Density"; <a href="http://www.irf.com/whats-new/nr000612a.html">http://www.irf.com/whats-new/nr000612a.html</a> ; June 2000; 2 pps.				
DF	AT		U.S. Patent Application Pub. No. US 2001/0038277 A1; Publication Date 11/8/2001; Burnstein et al.				
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